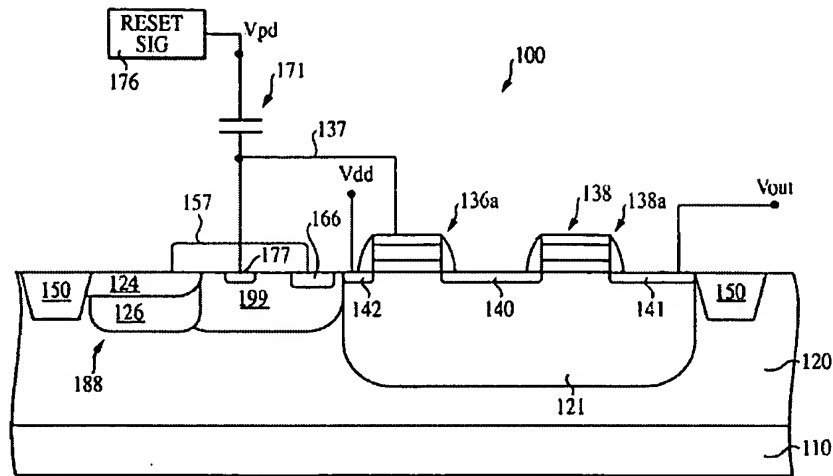
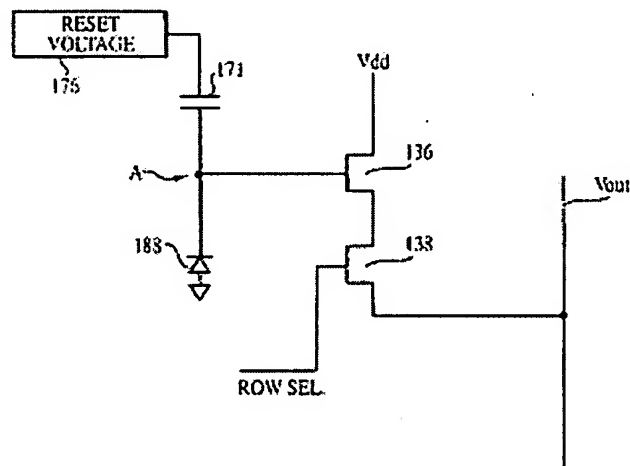


Specification FIG. 7



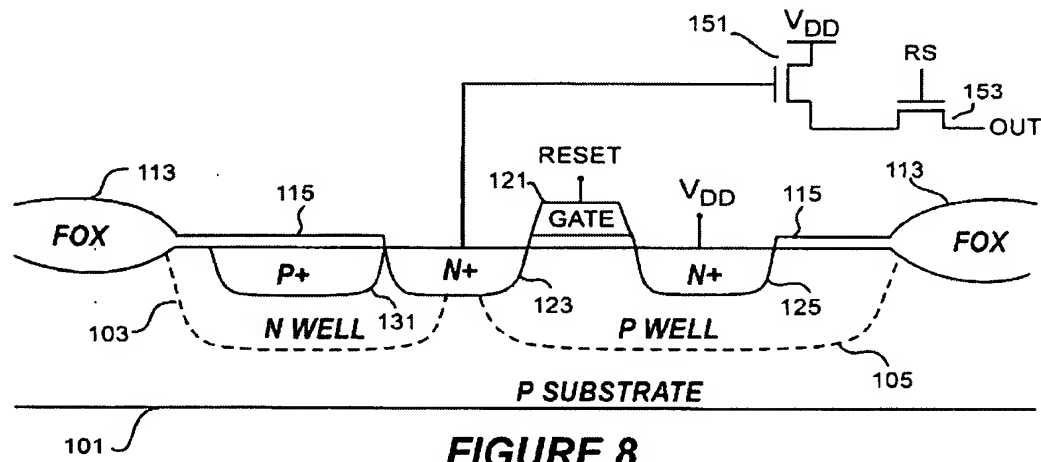
Specification FIG. 9



To the contrary, Zhao et al. teaches that the reset signal RESET is applied to reset gate 121, which is not in the P substrate 101 of the active pixel sensor device. FIG. 8 (reproduced below). Applicant respectfully submits that Zhao et al. does not disclose, teach, or suggest the reset region provided in the substrate and connected to the capacitor

as recited in claims 35 and 42. As described in the specification at paragraph [0005], the claimed invention allows the conventional reset transistor of Zhao et al. to be omitted.

Koichi et al. FIG. 14



**FIGURE 8**

Nor does Koichi et al. teach or suggest these limitations. Rather, Koichi et al. teaches in FIG. 14 (reproduced below) that “1101 is a first capacitor formed between the gate of the reset switch 114 and the gate of the MOS transistor 102.” Col. 15, ln. 51-53 (emphasis added). Koichi et al. further teaches that “1102 denotes a second capacitor formed between the gate of the MOS transistor 102 and ground.” Col. 15, ln. 55-57 (emphasis added). Therefore, capacitor 1102 cannot read on the capacitor of the claimed invention. Applicant respectfully submits that Koichi et al. does not disclose, teach, or suggest the reset region provided in the substrate and connected to the capacitor as recited in claims 35 and 42. As described in the specification at paragraph [0005], the claimed invention allows the conventional reset transistor 14, 104 of Koichi et al. to be omitted. Therefore, Koichi et al. does not remedy the deficiencies of Zhao et al.